

EAST Search History

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|-----|----|--|---|----|----|------------------|
| S28 | 0 | display\$6 same mode\$6 and pixel\$1 and substrate\$1 and (varY\$6 same freq\$6 and div\$6 same circuit\$6) and first same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and CPU and second same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and ((vary\$6 same freq\$6 and div\$6 same circuit\$6) and control\$6 same CPU) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 12:32 |
| S29 | 60 | display\$6 same mode\$6 and pixel\$1 and substrate\$1 and (varY\$6 same freq\$6 and div\$6 same circuit\$6) and ((vary\$6 same freq\$6 and div\$6 same circuit\$6) and control\$6 same CPU) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 12:32 |
| S30 | 60 | display\$6 same mode\$6 and pixel\$1 and substrate\$1 and ((vary\$6 same freq\$6 and div\$6 same circuit\$6) and control\$6 same CPU) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 12:36 |
| S31 | 0 | display\$6 same mode\$6 and pixel\$1 and substrate\$1 and ((vary\$6 same freq\$6 and div\$6 same circuit\$6) and control\$6 same CPU) and first same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and second same (thin same film same transistor or TFT) same form\$6 same substrate\$1 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 12:33 |
| S32 | 8 | display\$6 same mode\$6 and pixel\$1 and substrate\$1 and ((vary\$6 same freq\$6 and div\$6 same circuit\$6) and control\$6 same CPU) and (first same (thin same film same transistor or TFT) same form\$6 and second same (thin same film same transistor or TFT) same form\$6 and substrate\$1) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 12:34 |
| S33 | 0 | display\$6 same mode\$6 and pixel\$1 and substrate\$1 and ((vary\$6 same freq\$6 same div\$6 same circuit\$6) same control\$6 same CPU) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 12:36 |
| S34 | 0 | display\$6 same mode\$6 and pixel\$1 and substrate\$1 and (vary\$6 same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 12:37 |

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| S35 | 10 | display\$6 same mode\$6 and pixel\$1 and substrate\$1 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 15:18 |
| S36 | 61 | display\$6 same mode\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 15:48 |
| S37 | 11 | display\$6 same mode\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) and TFT | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 15:23 |
| S38 | 11 | display\$6 same mode\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) and (TFT or thin adj film adj transistor) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 15:24 |
| S39 | 10 | display\$6 same mode\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) and (TFT or thin adj film adj transistor) and substrate\$1 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 15:48 |
| S40 | 11 | display\$6 same mode\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) and (TFT or thin adj film adj transistor) and pixel\$1 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 15:25 |
| S41 | 89 | display\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 15:48 |
| S42 | 10 | display\$6 and mode\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) and (TFT or thin adj film adj transistor) and substrate\$1 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 15:49 |
| S43 | 10 | display\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) and (TFT or thin adj film adj transistor) and substrate\$1 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 15:49 |
| S44 | 44 | display\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) and substrate\$1 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 15:54 |

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| S45 | 13 | display\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) and substrate\$1 and pixel\$1 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/26 10:32 |
| S46 | 1 | "6339422".pn. and (TFT or thin adj film adj transistor) and pixel\$1 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 15:57 |
| S47 | 39 | display\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) and substrate\$1 and transistor\$1 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 15:55 |
| S48 | 11 | display\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) and substrate\$1 and transistor\$1 and pixel\$1 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 15:56 |
| S49 | 1 | "6339422".pn. and transistor and pixel\$1 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 15:57 |
| S50 | 0 | "6339422".pn. and matrix and pixel\$1 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 15:57 |
| S51 | 14 | display and pixel\$1 and substrate and variable same frequency same dividing same circuit\$6 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 15:58 |
| S52 | 20704 | display\$6 and (freq\$9 same divid\$6 same circuit\$6) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 16:45 |
| S53 | 164 | display\$6 and (freq\$9 same divid\$6 same circuit\$6 same TFT) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 16:45 |
| S54 | 199 | display\$6 and (freq\$9 same divid\$6 same circuit\$6 same (TFT or thin adj film adj transistor\$1)) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 16:46 |

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|-----|-----|---|---|----|----|------------------|
| S55 | 51 | display\$6 and (freq\$9 same divid\$6 same circuit\$6 same (TFT or thin adj film adj transistor\$1) same control\$6) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 16:46 |
| S56 | 2 | display\$6 and (freq\$9 same divid\$6 same circuit\$6 same (TFT or thin adj film adj transistor\$1) same control\$6 same cpu) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 16:47 |
| S57 | 24 | display\$6 and (freq\$9 same divid\$6 same circuit\$6 same (TFT or thin adj film adj transistor\$1) same control\$6 and cpu) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 19:35 |
| S58 | 23 | display\$6 and (freq\$9 same divid\$6 same circuit\$6 same (TFT or thin adj film adj transistor\$1) same control\$6 and cpu and substrate\$1 and pixel\$1) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 16:51 |
| S59 | 2 | display\$6 and (freq\$9 same divid\$6 same circuit\$6 same (TFT or thin adj film adj transistor\$1) same control\$6 same CPU) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 16:48 |
| S60 | 1 | "6339422".pn. and pixel\$1 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 16:48 |
| S61 | 3 | display\$6 and (variable and freq\$9 same divid\$6 same circuit\$6 same (TFT or thin adj film adj transistor\$1) same control\$6 and cpu and substrate\$1 and pixel\$1) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 16:56 |
| S62 | 1 | "20030067434" and (variable and freq\$9 same divid\$6 same circuit\$6 same (TFT or thin adj film adj transistor\$1) same control\$6) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 16:53 |
| S63 | 201 | display\$6 and variable and freq\$9 same divid\$6 same circuit\$6 and active same matrix | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 16:58 |
| S64 | 50 | display\$6 and variable same freq\$9 same divid\$6 same circuit\$6 and active same matrix | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 19:35 |

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| S65 | 32 | display\$6 and variable same freq\$9 same divid\$6 same circuit\$6 and active same matrix and cpu | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 16:58 |
| S66 | 13 | display\$6 and variable same freq\$9 same divid\$6 same circuit\$6 and active same matrix and "345"/\$.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 17:12 |
| S67 | 1 | "5859626".pn. and Ic\$3 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 17:28 |
| S68 | 3 | "20020036636" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 19:35 |
| S69 | 0 | "20020036636" and display\$6 and (freq\$9 same divid\$6 same circuit\$6 same (TFT or thin adj film adj transistor\$1) same control\$6 and cpu) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 19:36 |
| S70 | 2 | "20020036636" and display\$6 and freq\$9 same divid\$6 same circuit\$6 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 19:40 |
| S71 | 0 | "20020036636" and display\$6 and freq\$9 same divid\$6 same circuit\$6 same (TFT or thin adj film adj transistor\$1) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 19:36 |
| S72 | 0 | "20020036636" and display\$6 and freq\$9 same divid\$6 same control\$6 and cpu | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 19:36 |
| S73 | 2 | "20020036636" and display\$6 and freq\$9 same divid\$6 same circuit\$6 and substrate | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 19:40 |
| S74 | 2 | "20020036636" and freq\$9 same divid\$6 same circuit\$6 and substrate | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 19:44 |

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|-----|----|---|---|----|----|------------------|
| S75 | 2 | "20010007432" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 19:42 |
| S76 | 2 | "20010007432" and substrate\$1 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 19:43 |
| S77 | 0 | "20010007432" and substrate\$1 and control\$6 and cpu | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 19:43 |
| S78 | 0 | "20010007432" and substrate\$1 and control\$6 and (cpu or microprocessor or controller) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 19:44 |
| S79 | 3 | "20020036636" and control\$6 and (cpu or microprocessor or controller) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 19:45 |
| S80 | 0 | "20020036636" and control\$6 and (cpu or microprocessor or controller) and substarte\$1 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 19:45 |
| S81 | 2 | 11-202290 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 20:43 |
| S82 | 30 | "202290" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 20:42 |
| S83 | 3 | "11202290" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 20:44 |
| S84 | 5 | display\$6 and pixel\$1 same substrate\$1 same charge\$6 same pump\$6 same driv\$6 same circuit\$6 and (freq\$9 and divid\$6 and circuit\$6) and diode\$1 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 23:06 |

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| S85 | 5 | display\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) and substrate\$1 and pixel\$1 and EL | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/26 10:25 |
| S86 | 5 | display\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) and substrate\$1 and pixel\$1 and (EL or electroluminescent) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/26 10:25 |
| S87 | 3 | "20020154151" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/26 10:29 |
| S88 | 0 | "20020154151" and (el or electroluminescent or electro-luminescent or electro adj luminescent) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/26 10:30 |
| S89 | 1 | "20020154151" and diode | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/26 11:53 |
| S90 | 8 | display\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) and substrate\$1 and pixel\$1 and OLED | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/26 11:08 |
| S91 | 8 | display\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) and substrate\$1 and pixel\$1 and OLED and transistor | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/26 11:09 |
| S92 | 8 | display\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) and substrate\$1 and pixel\$1 and OLED and transistor\$1 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/26 11:13 |
| S93 | 0 | display\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) and substrate\$1 and pixel\$1 and OLED and thdisplay\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) and substrate\$1 and pixel\$1 and OLED and transistor\$1transistor\$1 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/26 11:10 |

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| S94 | 8 | display\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) and substrate\$1 and pixel\$1 and OLED and thin same film same transistor\$1 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/26 11:11 |
| S95 | 8 | display\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) and substrate\$1 and pixel\$1 and OLED and thin same film same transistor\$1 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/26 12:07 |
| S96 | 11 | display\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) and substrate\$1 and pixel\$1 and ratio | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/26 11:52 |
| S97 | 2 | "20020154151" and ratio | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/26 11:53 |
| S98 | 8 | display\$6 and (variable same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU) and substrate\$1 and pixel\$1 and (LCD or liquid adj crystal adj display) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/26 12:08 |
| S99 | 2 | "7138975".pn. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/26 12:11 |
| S100 | 1 | "7138975".pn. and (LCD or liquid adj crystal adj display) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/26 12:24 |
| S101 | 1 | "7138975".pn. and EL | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/26 12:28 |
| S102 | 1 | "7138975".pn. and EL and mim | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/26 12:28 |

EAST Search History

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|---|---|------------------|---------|------------------|
| L2 | 13 | display\$6 and pixel\$1 same substrate\$1 and (vari\$6 and frequency\$6 same divid\$6 same circuit\$6) same substrate and CPU same substrate | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/09/24 07:25 |
| L3 | 2 | display\$6 and pixel\$1 same substrate\$1 and (frequency\$6 same divid\$6 same circuit\$6) same substrate and CPU same substrate and charge\$6 SAME PUMP\$6 SAME CIRCUIT\$6 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/09/24 07:26 |
| L4 | 2 | display\$6 and pixel\$1 same substrate\$1 and (frequency\$6 same divid\$6 same circuit\$6) same substrate and CPU same substrate and charge\$6 SAME PUMP\$6 SAME CIRCUIT\$6 SAME SUBSTRATE | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/09/24 07:26 |
| L5 | 3 | "20020036636" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/09/24 09:15 |
| L6 | 2 | "6,909,413".PN. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/09/24 09:09 |
| L7 | 2 | "6,909,413".PN. and substrate\$1 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/09/24 09:15 |
| L8 | 1 | "6,909,413".PN. and substrate\$1 and CPU | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/09/24 09:15 |
| L9 | 0 | "20020036636" and CPU | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/09/24 09:16 |
| L10 | 0 | "20020036636" and (CPU or computer or microprocessor or processor) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/09/24 09:16 |

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|----|---|--|---|----|----|------------------|
| S1 | 6 | display\$6 same pixel\$1 same substrate\$1 and vari\$6 same frequency\$6 same divid\$6 same circuit\$6 and thin same film same transistor same substrate and CPU same substrate | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/09/23 22:09 |
| S2 | 2 | display\$6 and pixel\$1 same substrate\$1 and (vari\$6 same frequency\$6 same divid\$6 same circuit\$6 and thin same film same transistor) same substrate and CPU same substrate | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/09/24 07:17 |

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| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|--|---|------------------|---------|------------------|
| L1 | 1960 | display\$6 and (variable and freq\$9 same divid\$6 same circuit\$6 and active same matrix | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 16:58 |
| | | "20030067434" and (variable and freq\$9 same divid\$6 same circuit\$6 same (TFT or thin adj film adj transistor\$1) same control\$6 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 16:53 |
| | | display\$6 same mode\$6 and pixel\$1 and substrate\$1 and var\$9 same freq\$6 same div\$9 same circuit\$6 and first same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and CPU and second same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and vary\$9 same freq\$9 same div\$6 same circuit\$6 same control\$6 same CPU | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 12:27 |
| | | OCB same display | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/09/24 09:28 |
| L2 | 656 | OCB same display and active same matrix | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/09/24 09:29 |
| L3 | 149 | OCB same display same active same matrix and pixel\$1 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/09/24 09:29 |
| S1 | 0 | display and pixel\$1 and substrate and variable same frequency same dividing same circuit\$6 and first same thin same film same transistor same form\$6 same substrate\$1 and CPU and second same thin same film same transistor same form\$6 same substrate\$1 and variable same frequency same dividing same circuit\$6 same control\$6 same CPU and divid\$6 same ratio same var\$6 same according same display\$6 same mode\$1 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/22 13:05 |

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|----|---|--|---|----|----|------------------|
| S3 | 0 | display and pixel\$1 and substrate and variable same frequency same dividing same circuit\$6 and first same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and CPU and second same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and variable same frequency same dividing same circuit\$6 same control\$6 same CPU and divid\$6 same ratio same var\$6 same according same display\$6 same mode\$1 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/22 15:59 |
| S4 | 3 | display and pixel\$1 and substrate and variable same frequency same dividing same circuit\$6 and first same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and CPU and second same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and variable same frequency same dividing same circuit\$6 same control\$6 same CPU | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 12:21 |
| S5 | 3 | display and pixel\$1 and substrate and variable same frequency same dividing same circuit\$6 and first same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and CPU and second same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and variable same frequency same dividing same circuit\$6 same control\$6 same CPU and mode | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/22 15:34 |
| S6 | 3 | "20030063077" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/22 15:35 |
| S7 | 0 | "20030063077" and mode same display\$6 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/22 15:35 |
| S8 | 2 | "20030063077" and mode same display\$6 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/22 15:50 |

EAST Search History

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| S9 | 2 | "20030063077" and mode same display\$6 and frequency\$6 same div\$6 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/22 15:50 |
| S10 | 0 | display and pixel\$1 same substrate\$1 same charge same pumpo | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 12:08 |
| S11 | 29 | display and pixel\$1 same substrate\$1 same charge same pump | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/22 16:00 |
| S12 | 16 | display and pixel\$1 same substrate\$1 same charge same pump same driv\$6 same circuit\$6 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/22 16:01 |
| S13 | 3 | display same mode and pixel\$1 and substrate and variable same frequency same dividing same circuit\$6 and first same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and CPU and second same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and variable same frequency same dividing same circuit\$6 same control\$6 same CPU | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 12:07 |
| S14 | 3 | display\$6 same mode\$6 and pixel\$1 and substrate and variable same frequency same dividing same circuit\$6 and first same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and CPU and second same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and variable same frequency same dividing same circuit\$6 same control\$6 same CPU | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 12:25 |
| S15 | 34 | display\$6 and pixel\$1 same substrate\$1 same charge\$6 same pump\$6 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 12:09 |

EAST Search History

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| S16 | 16 | display\$6 and pixel\$1 same substrate\$1 same charge\$6 same pump\$6 same driv\$6 same circuit\$6 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 12:09 |
| S17 | 9 | display\$6 and pixel\$1 same substrate\$1 same charge\$6 same pump\$6 same driv\$6 same circuit\$6 and freq\$9 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 12:14 |
| S18 | 5 | display\$6 and pixel\$1 same substrate\$1 same charge\$6 same pump\$6 same driv\$6 same circuit\$6 and freq\$9 same divid\$6 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 12:13 |
| S19 | 2 | display\$6 and pixel\$1 same substrate\$1 same charge\$6 same pump\$6 same driv\$6 same circuit\$6 and freq\$9 same divid\$6 same circuit\$6 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 12:15 |
| S20 | 8 | display\$6 and pixel\$1 same substrate\$1 same charge\$6 same pump\$6 same driv\$6 same circuit\$6 and (freq\$9 and divid\$6 and circuit\$6) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 23:06 |
| S21 | 7 | display\$6 and pixel\$1 same substrate\$1 same charge\$6 same pump\$6 same driv\$6 same circuit\$6 and (freq\$9 and vari\$9 and circuit\$6) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 12:20 |
| S22 | 2 | display\$6 and pixel\$1 same substrate\$1 same charge\$6 same pump\$6 same driv\$6 same circuit\$6 and (freq\$9 same vari\$9 and circuit\$6) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 12:20 |
| S23 | 3137 | display and pixel\$1 and substrate\$1 and frequ\$8 same divi\$6 or vary\$6 same circuit\$6 and first same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and CPU and second same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and vari\$6 same freq\$9 same divi\$6 same circuit\$6 same control\$6 same CPU | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 12:23 |

EAST Search History

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| S24 | 0 | display\$6 and pixel\$1 and substrate\$1 and vary\$6 same freq\$8 same divi\$6 same circuit\$6 and first same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and CPU and second same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and vari\$6 same freq\$9 same divi\$6 same circuit\$6 same control\$6 same CPU | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 12:24 |
| S25 | 0 | display\$6 same mode\$6 and pixel\$1 and substrate\$1 and var\$6 same freq\$6 same div\$6 same circuit\$6 and first same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and CPU and second same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and vary\$6 same freq\$6 same div\$6 same circuit\$6 same control\$6 same CPU | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 12:28 |
| S26 | 0 | display\$6 same mode\$6 and pixel\$1 and substrate\$1 and (var\$6 same freq\$6 and div\$6 same circuit\$6) and first same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and CPU and second same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and (vary\$6 same freq\$6 and div\$6 same circuit\$6) same control\$6 same CPU | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 12:30 |
| S27 | 0 | display\$6 same mode\$6 and pixel\$1 and substrate\$1 and (var\$6 same freq\$6 and div\$6 same circuit\$6) and first same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and CPU and second same (thin same film same transistor or TFT) same form\$6 same substrate\$1 and ((vary\$6 same freq\$6 and div\$6 same circuit\$6) and control\$6 same CPU) | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2007/03/25 12:31 |